

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hajime Kimura et al. Art Unit : Unknown
Serial No. : New Application Examiner : Unknown
Filed : January 14, 2004
Title : A CURRENT SOURCE CIRCUIT, A SIGNAL LINE DRIVER CIRCUIT AND A
DRIVING METHOD THEREOF AND A LIGHT EMITTING DEVICE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Applicants submit the references listed on the attached form PTO-1449.

This statement is being filed with the application. Please apply any charges or credits to
Deposit Account No. 06-1050.

Respectfully submitted,

Date: January 14, 2004



John F. Hayden
Reg. No. 37,640

Customer No. 26171
Fish & Richardson P.C.
1425 K Street, N.W., 11th Floor
Washington, DC 20005-3500
Telephone: (202) 783-5070
Facsimile: (202) 783-2331

Substitute Form PTO-1449 (Modified) Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 12732-207001	Application No. New Application
	Applicant Hajime Kimura et al.		
	Filing Date January 14, 2004	Group Art Unit	

U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

Foreign Patent Documents or Published Foreign Patent Applications								
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AL	2003-066909	03/05/2003	JAPAN			ABS	
	AM	2003-150112	05/23/2003	JAPAN			ABS	
	AN	2003-195812	07/09/2003	JAPAN			ABS	
	AO							
	AP							

Other Documents (include Author, Title, Date, and Place of Publication)		
Examiner Initial	Desig. ID	Document
	AQ	Reiji Hattori et al.; "Circuit Simulation of Poly-Si TFT Based Current-writing Active-Matrix Organic LED Display"; <i>Technical Report of IEICE, ED2001-8, SDM2001-8</i> ; The Institute of Electronics, Information and Communication Engineers, pp. 7-14; 2001
	AR	
	AS	
	AT	

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	